

CRISP V3.1 STATUS

Changes since last report

A bug in VALUE_C16 has been fixed. This caused logics to access-violate randomly after VALUE_C16 calls.

The migration manual and test plan documents have been revised to reflect the fact that they are not Alpha-only.

Current Task Assignments

Stan

- RJR reported a problem with NEW_DESTINATION.
- Update header files to be fully C++ compatible.

Jim

- (none)

RJR

- Execute the test plan.
- Create a much shorter version of the D26 logic which demonstrates the long compile time problem for research by RJR.
- Get Compaq to release the fixed macro compiler as an official ECO.

Future

- On-line database swapping is on hold for now.

Pre-Existing Problems

- In a redundant system on VAX with I/Onyx, total loss of both module servers can cause 100% CPU utilization because there is no backoff when re-enable is set. Classic I/O over CC Server doesn't have this problem.
- In PCWS, jogging of numerics and longs goes by 1100, and jogging of floats goes by 110.0.
- Logic call STR_MERGE: When source, source position, source length, and destination is supplied, the text is appended to the wrong position in the string. The appended text overwrites the last character in the destination string.